

Design and Analysis of Shunt Active Power Filter (SAPF) for Non Linear and Unbalanced Loads

Kiran Yaddanapudi, Pannala Krishna Murthy

Abstract: Increasing power demand in present situation has to comply with the power generation. Generated power has to meet the quality standards. With the introduction of nonlinear loads like static power converters inject harmonics into the system which effect operation of the system adversely. In order to improve the performance of the system these harmonics are to be mitigated and reactive power need to be compensated. In the proposed work a Shunt Active Power Filter (SAPF) is designed to compensate reactive power and the behavior of the system is analyzed by introducing reverse harmonic current.

Keywords: Shunt Active Power Filter, Instantaneous Power Theory, Clarkes Transformation, Total Harmonic Distortion (THD), Pulse Width Modulation.

I. INTRODUCTION

Advent of power electronic technology has given scope for development of non linear loads such as diode-rectifiers, thyristor converters, adjustable speed drives, furnaces, computer power supplies, un-interruptible power supplies, etc. which are economical, flexible and energy efficient but effect the power quality by producing voltage harmonics from current harmonics and consume extra reactive power.[1] Extensive research has been done on active filters since their inception in 1970. Improvements in power electronics technology has made engineers to use active filters practically. Many numbers of shunt active filters made up of Pulse Width Modulation (PWM) inverters using insulated biplolar transistors (IGBT) or gate turn-off (GTO) thyristors are operating successfully. Usually, passive LC filters are put to use to mitigate line current harmonics and for power factor improvement. But there are so many drawbacks of passive filters such as they are bulky in size, compensation is fixed, and causing resonance. To overcome the drawbacks of passive filters, active power filters were designed. [3]

Modern active filters are better in terms of their behavior, small size, and their flexibility in application than traditional passive filters. In addition, the active filters are available at lower cost with low operating loss, than passive filters. [2]

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II. ACTIVE FILTERS

Active Power Filters (APF) are used in low power (<100kVA), medium power (100kVA-10MVA) and high power (>10 MVA) applications [7]. The power circuit of Active Power Filters is generally made up of by a DC Capacitor Unit, DC/AC converter and Harmonic filter.

A. DC Capacitor Unit

The purposes of DC Capacitor Unit are twofold, firstly during steady state a DC Voltage is maintained with small ripple. Secondly during transient period it supplies real power difference between load and source behaving like an energy storage element.

B. DC/AC Converter

The purposes of DC Capacitor Unit are twofold, firstly during steady state a DC Voltage is maintained with small ripple. Secondly during transient period it supplies real power difference between load and source behaving like an energy storage element.

C. Harmonic Filter

An inductance for output filtering of VSI is used to mitigate the harmonic at various frequencies. Attenuation of switching ripple currents with different combinations of L and C filters was examined in [8]. A malfunction or shutdown of a rectifier happens if it has extra low inductance characteristic or connected with load having high frequency input current with phase control. To reduce the rising rate of load input current for above load a reactor of 3 – 5 % has to be connected at the input side of the load. [9].For reactive power compensation and mitigation of harmonics LC passive filter is used in [10]. To attain required damping of switching harmonics smaller inductors can be employed in LCL-filter presented in [11] when compared with L-filter because of advantages in costs and dynamic performance. [5]

III. TYPES OF ACTIVE POWER FILTERS

Active Power Filters can be connected in several power circuit configurations. Generally, they are classified into three categories, they are Shunt Active Power Filters, Series Active Power Filters and Hybrid Active Power Filters [12][13].



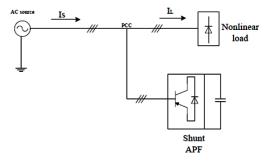


Fig. 1.Shunt Active Power Filter

A. Shunt Active Power Filter

This class of filter configurations is the most important and most widely used type in active filtering applications. Shunt Active Power Filter is shown in Fig 1. Its purpose is to cancel the load current harmonics fed to the supply. Balancing three phase currents and compensation of reactive power are its contributions. To compensate for system losses a small amount of active fundamental current plus the compensation current is supplied by Parallel filters. For wide range of power ratings a number of filters can be connected in parallel to get higher currents.

B. Series Active Power Filter

The active filter in this configuration produces a Pulse Width Modulated voltage waveform which is added or subtracted, on an instantaneous basis, to/from the supply voltage to maintain a pure sinusoidal voltage waveform across the load. In general Series active filters are used hardly in industries, when compared to parallel active filters. High current ratings in the secondary side of the coupling transformer since these filters have to carry high load currents is the main drawback. These are employed for balancing three phase voltages and to reduce the voltage harmonics. These are preferred for voltage sensitive devices.

C. Hybrid Active Power Filters

These configurations can overcome the technical limitations of conventional Active Power Filters. Hybrid APFs are made up with both APFs and passive filters. With the advantages of both passive and active filters cost-effective solutions with improved performance were provided by Hybrid APFs. In addition to conventional APF a low cost passive high pass filter is used in this scheme. These two filters divide the task of filtering between them. Lower order harmonics were handled by APF while the higher order harmonics by passive filters. Improved filtering performance of higher order harmonics with cost effective low order harmonics mitigation are provided by hybrid APF. [4]

IV. INSTANTANEOUS POWER THEORY

The control of active power filters in real time is done by the p-q theory or instantaneous power theory in time domain; it can be used either in steady-state or transient state also for generic voltage and current waveforms. The p-q theory performs a Clarke transformation of a stationary reference system of coordinates a-b-c to a reference system of coordinates $\alpha-\beta-0$, also stationary. In ABC coordinates axes are set on the same plane, displaced from each other by 120° .

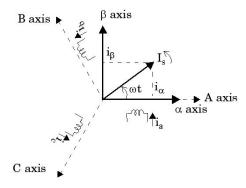


Fig. 2.abc to αβ0 Transformation

$$\begin{bmatrix} \dot{l}_{\alpha} \\ \dot{l}_{\beta} \\ \dot{l}_{0} \end{bmatrix} = \sqrt{\frac{2}{3}} \begin{bmatrix} 1 & -\frac{1}{2} & -\frac{1}{2} \\ 0 & \frac{\sqrt{3}}{2} & -\frac{\sqrt{3}}{2} \\ \frac{1}{\sqrt{2}} & \frac{1}{\sqrt{2}} & \frac{1}{\sqrt{2}} \end{bmatrix} \begin{bmatrix} \dot{l}_{\alpha} \\ \dot{l}_{b} \\ \dot{l}_{c} \end{bmatrix}$$
(1)

$$\begin{bmatrix} \dot{i}_{a} \\ \dot{i}_{b} \\ \dot{i}_{c} \end{bmatrix} = \sqrt{\frac{2}{3}} \begin{bmatrix} 1 & 0 & \frac{1}{\sqrt{2}} \\ -\frac{1}{2} & \frac{\sqrt{3}}{2} & \frac{1}{\sqrt{2}} \\ -\frac{1}{2} & -\frac{\sqrt{3}}{2} & \frac{1}{\sqrt{2}} \end{bmatrix} \begin{bmatrix} \dot{i}_{\alpha} \\ \dot{i}_{\beta} \\ \dot{i}_{0} \end{bmatrix}$$
 (2)

$$\begin{bmatrix} v_{\alpha} \\ v_{\beta} \\ v_{0} \end{bmatrix} = \sqrt{\frac{2}{3}} \begin{bmatrix} 1 & -\frac{1}{2} & -\frac{1}{2} \\ 0 & \frac{\sqrt{3}}{2} & -\frac{\sqrt{3}}{2} \\ \frac{1}{\sqrt{2}} & \frac{1}{\sqrt{2}} & \frac{1}{\sqrt{2}} \end{bmatrix} \begin{bmatrix} v_{a} \\ v_{b} \\ v_{c} \end{bmatrix}$$
(3)

$$\begin{bmatrix} v_a \\ v_b \\ v_c \end{bmatrix} = \sqrt{\frac{2}{3}} \begin{bmatrix} 1 & 0 & \frac{1}{\sqrt{2}} \\ -\frac{1}{2} & \frac{\sqrt{3}}{2} & \frac{1}{\sqrt{2}} \\ -\frac{1}{2} & -\frac{\sqrt{3}}{2} & \frac{1}{\sqrt{2}} \end{bmatrix} \begin{bmatrix} v_a \\ v_\beta \\ v_0 \end{bmatrix}$$
(4)

where i_a , i_b , i_c are the load currents and v_a , v_b , v_c are the load voltages

The equations of active, reactive and zero-sequence powers are defined as in equations.

$$p = v_{\alpha}i_{\alpha} + v_{\beta}i_{\beta} \tag{5}$$

$$q = v_{\alpha}i_{\beta} - v_{\beta}i_{\alpha} \tag{6}$$

$$p_0 = v_0 i_0 \tag{7}$$

The currents, voltages and powers in the α - β system can be decomposed in mean and alternating values, in terms of the fundamental and harmonic components, as in equation

$$x = \bar{x} + \tilde{x} \tag{8}$$

Where x can be powers, currents, or voltages.

The power components have the following physical meaning [14]

 p_0 Zero Sequence Power

 \overline{p} Mean Value of the Instantaneous Real Power

 \tilde{p} Alternating Value of the Instantaneous Real Power

 \overline{q} Mean Value of the Imaginary Power

 \tilde{q} Alternating Value of the Imaginary Power

The powers required to be compensated by the APF are calculated as in equation





$$\begin{bmatrix} \tilde{p} \\ q \end{bmatrix} = \begin{bmatrix} \overline{v_{\alpha}} & \overline{v_{\beta}} \\ 0 & 0 \end{bmatrix} \begin{bmatrix} \tilde{\iota_{\alpha}} \\ \tilde{\iota_{\beta}} \end{bmatrix} + \begin{bmatrix} 0 & 0 \\ -v_{\beta} & \overline{v_{\alpha}} \end{bmatrix} \begin{bmatrix} i_{\alpha} \\ i_{\beta} \end{bmatrix}$$
(9)

The reference currents are calculated by adding the active power required to regulate the DC bus voltage, ploss to the alternative value of instantaneous real power from the following equation

$$\begin{bmatrix}
i_{\alpha}^{*} \\
i_{\beta}^{*}
\end{bmatrix} = \frac{1}{\Delta} T \begin{bmatrix} 0 \\ \bar{q} \end{bmatrix} + \frac{1}{\Delta} T \begin{bmatrix} \tilde{p} + p_{loss} \\ \tilde{q} \end{bmatrix}$$
Where $\Delta = \overline{v_{\alpha}^{2}} + \overline{v_{\beta}^{2}}$ and $T = \begin{bmatrix} \overline{v_{\alpha}} & -\overline{v_{\beta}} \\ \overline{v_{\beta}} & \overline{v_{\alpha}} \end{bmatrix}$ (10)

Where
$$\Delta = \overline{v_{\alpha}^2} + \overline{v_{\beta}^2}$$
 and $T = \begin{bmatrix} \overline{v_{\alpha}} & -\overline{v_{\beta}} \\ \overline{v_{\beta}} & \overline{v_{\alpha}} \end{bmatrix}$

From (9) the APF computes \tilde{p} using the harmonic components of the currents while $q = \bar{q} + \tilde{q}$ are computed using the load current, including AC and DC components, according to Fig.

Transformation of load currents from three-phase abc to $\alpha\beta0$ components is done using Clarke transformation, as in (11).

components is done using Clarke transformation, as in (11).
$$\begin{vmatrix} i_a^* \\ i_b^* \\ i_c^* \end{vmatrix} = \sqrt{\frac{2}{3}} \begin{bmatrix} 1 & 0 & \frac{1}{\sqrt{2}} \\ -\frac{1}{2} & \frac{\sqrt{3}}{2} & \frac{1}{\sqrt{2}} \\ -\frac{1}{2} & -\frac{\sqrt{3}}{2} & \frac{1}{\sqrt{2}} \end{bmatrix} \begin{bmatrix} i_a^* \\ i_\beta^* \\ i_0^* \end{bmatrix}$$
Shunt active power filter provides compensation strategy based on the p a theory of all undesired power components.

Shunt active power filter provides compensation strategy based on the p-q theory of all undesired power components $(p, p_0 \text{ and } q). [4]$

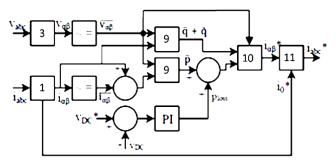


Fig. 3. Proposed Control Strategy

DESIGN OF SHUNT ACTIVE POWER FILTER **USING MATLAB**

A Shunt Active Power Filter is designed with MATLAB/Simulink as shown in the Fig. 4.

The model consists of a three phase source supplying a phase to phase RMS voltage of 400Volts, 50Hz with Yg having internal connection a source resistance Rs=0.0010hms, source inductance Ls=1e-8Henries, series R=0.010hms and series resistance inductance L=1e-6Henries.

Two types of loads are connected to the system, they are firstly a rectifier with a load resistance of 10 Ohms and a three phase unbalanced star connected resistive load with 10 Ohms, 20 Ohms & 30 Ohms in the respective a, b, & c phases.

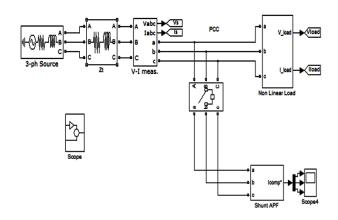


Fig. 4.Simulink Model of Shunt Active Power Filter

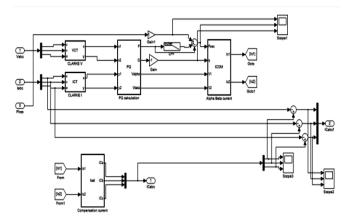


Fig. 5.Simulink Model of Control of Shunt Active Power **Filter**

VI. SIMULATION RESULTS

Shunt Active Power Filter MATLAB model is simulated by connecting non linear load from starting and the unbalanced load at T=0.1 seconds with a sampling time of Ts=5e-6seconds without connecting filter and the performance of the system is analyzed by the output waveforms and the Total Harmonic Distortion (THD) of the signals are calculated. The output waveforms are shown in Fig. 6 & Fig. 7.

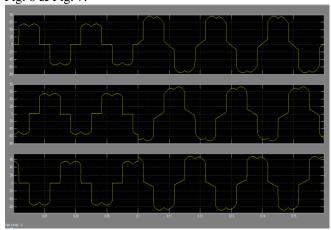


Fig. 6. Three Phase Supply Current without Filter



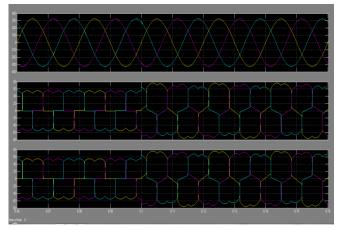


Fig. 7. Three Phase Load Voltage, Load Current and Supply Current without Filter

Now Shunt Active Power Filter is connected to the model with a Transition time of T=0.01 seconds and the model is simulated again, it was observed that at T=0.01 seconds the filter got turned on and the process of mitigating current harmonic has started and the performance of the system started to improve. The results from the simulation were observed from the waveforms and the Total Harmonic Distortion (THD) of the signals. The output waveforms after connecting the filter were shown in Fig. 8, Fig. 9 & Fig.10.

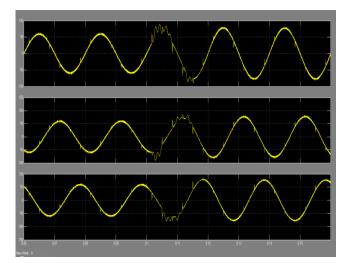


Fig. 8. Three Phase Supply Current with Filter

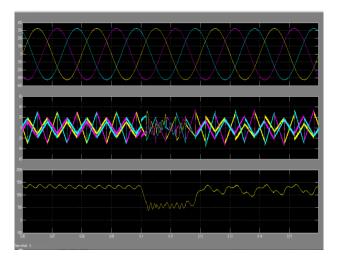


Fig. 9. Three Phase Supply Voltage, Filter Current and DC Voltage with Filter

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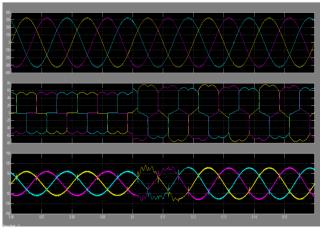


Fig. 10. Three Phase Load Voltage, Load Current and Supply Current with Filter

Table- I: Total Harmonic Distortion of Supply Current with and without filter for Non Linear and Unbalanced Loads.

Signal	Signal Number	THD in % without filter	THD in % with filter
NON LINEAR LOAD & UNBALANCED LOAD			
Is	1	21.88	3.31
	2	23.11	3.79
	3	24.59	3.70
NON LINEAR LOAD			
Is	1	30.26	4.60
	2	30.26	4.48
	3	30.26	4.60

VII. CONCLUSIONS

Shunt Active Power Filter (SAPF) model is simulated with MATLAB/Simulink and it is observed that with SAPF the Total Harmonic Distortion (THD) of the supply current (Is) is reduced from 21.88% to 3.31% for non linear load and unbalanced load and from 30.26% to 4.60% with only non linear load. So the performance of the system is improved by compensating reactive power and mitigating the harmonics.

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Kiran Yaddanapudi was born in 1980 in Kallur, India. He received the B.Tech in Electrical & Electronics Engineering from Kakatiya University in 2001 and M.Tech in Embedded Systems in JNTUH, Hyderabad in 2011. He is at present pursuing Ph.D. in Department of EEE, JNTUH, Hyderabad. His research areas include Power Systems, Power Quality, HVDC and Filters.



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